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High efficiency surface-conducted field emission from a ZnO nanotetrapod and MgO nanoparticle composite emitter

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We report a surface-conducted field emitter made of a ZnO nanotetrapod and MgO nanoparticle composites with a high emission efficiency ($\sim 100\%$) and current (3.77 mA at a gate voltage of 100 V and anode voltage of 1800 V). The fabrications of the triode structure with a 10×10 pixel array and corresponding driving method have been proposed. The electron trajectories are simulated according to the structure. Individual pixel addressing can be achieved by a sequential scanning mode. Display of moving images employing this triode structure was demonstrated. The results are of significance to the development of ZnO based triode field emitters. © 2008 American Institute of Physics. [DOI: 10.1063/1.3046785]

Cold cathode field emission (FE) has attracted much attention for its application in a wide range of FE devices such as field emission display (FED), x-ray source, vacuum microwave amplifiers, etc. Over the years, various kinds of cold cathodes have been developed, for example, Spindt-type field emitter arrays, metal-insulator-metal cathode, and one-dimensional nanomaterial FE cathode.

Recently, Geis *et al.*^{4,5} reported a FE with an ultralow electric field of 10 V/cm from a surface emission cathode. Nomura *et al.*⁶ reported a practical surface-conducted field emission (SCFE) cathode in 1996, based on which, Canon and Toshiba have showcased a 36 in. surface-conduction emission display panel in 2005. However, low emission efficiency is a serious drawback to realize practical and commercial SCFE panel. The emission efficiency of previously reported cathodes based on surface emission mechanism is in the range 0.1%–10%. 7,9

Very recently, we have reported a SCFE cathode made of ZnO nanotetrapod (ZNT) films with an emission efficiency of 60% (the efficiency in this paper refers to the percentage ratio of the anode current over the sum of the anode and gate current). Although the efficiency is much higher than those previously reported, there is still much room to improve to reach the ultimate efficiency of 100%. In this paper, we shall present an improved SCFE cathode made of ZNT and MgO nanoparticle composite with an efficiency close to 100%.

Figure 1 shows the schematic image of the screen-printed SCFE triode configuration and the pixel array. We use a 3 mm thick float law glass as the cathode substitute for making the triode structure. Before printing the Ag electrodes, the glass is cleaned in de-ionized water and ethanol. The cathode is composed of two vertical electrodes (25 μ m thick, on the top called gate electrode, another called cathode electrode) separated by a 100 μ m surface-conduction channel, with an area of 0.008 cm² (the width of the cathode electrode is 8 mm). The gates and cathodes are formed as

Figure 2(a) shows a low magnification scanning electron microscopy (SEM) top-view image of as-fabricated cathode, where we can identify the Ag and ZNT/MgO regions. Figure

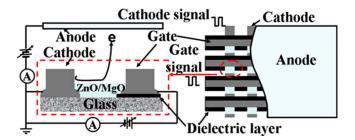


FIG. 1. (Color online) The schematic image of the SCFE triode configuration.

stripes by some Ag paste for vacuum fluorescent display (VFD) (~70%) from Guizhou Zhenhuaasia Pacific High Tech Electronic Materials Co., Ltd. The surface-conduction channel is made of ZNT and MgO nanoparticle composites. MgO nanoparticles (≥98.5%, Analytical Reagent) used in this experiment were purchased commercially from Sinopharm Chemical Reagent Co., Ltd, and ZNTs were synthesized without any catalyst by vapor phase transport method in a horizontal tube furnace. 11 The ZnO-MgO (2:1 weight ratio) nanocomposite paste disperse in organic binders, which consisted of terpineol and ethyl cellulose using a homogenizer. The fabrication is described as follows. Cathode electrodes were screen-printed using Ag paste on a glass substrate, sintered at 560 °C for 4 h and cooled down to the room temperature. The dielectric layers were formed on the top of the cathode vertically. Subsequently, the gate electrodes were screen-printed on the dielectric layer as the same fabrication of cathode electrodes. In order to contact the two electrodes, ZnO-MgO paste was coated on the space between them. After baking at 500 °C for 30 min in a conventional oven in air and cooled down naturally, all organic vehicles were removed and a ZnO-MgO nanocomposite film was formed and also adhered well to the glass and Ag elec-

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FIG. 2. (a) Low magnification SEM top-view picture of the ZNT/MgO film. (b) High magnification SEM top-view picture of the ZNT/MgO film in the rectangle of (a). (c) SEM cross-sectional picture of the ZNT/MgO film. (d) High magnification SEM of the triple junction.

2(b) shows a high magnification SEM image of a ZNT/MgO film in the rectangle of (a), where uniformly dispersed ZNTs and MgO nanopowders can be clearly seen. Figure 2(c) shows the cross section of the cathode, where the needle of the ZNT is perpendicular to the cathode plane. Figure 2(d) shows a high magnification SEM of Fig. 2(c).

FE testing was performed in a vacuum chamber at a pressure of $\sim 10^{-6}$ Pa. The space between the anode and the cathode was controlled by a 3 mm spacer. Electrical annealing (a few hundreds of volts) was applied on both the anode and gate electrode before real testing. According to Fig. 1, a driving scheme for such a SCFE structure can be explained by a 10×10 pixel array. The device operates at a sequential scanning mode. The gate signal corresponds to scan signal (in row), and the cathode signal corresponds to the modulation signal (in column). For the nonaddressed row, the scan signal is zero, and all the rows of pixels are eliminated to the emitter. While for the address row, the scan signal is biased to the positive. In order to turn on the pixel, a negative bias is applied to the modulation signal. On the contrary, the modulation is biased to a positive to close the pixel.

The dependencies of the anode current and the gate current on gate voltage at different anode voltages are shown in Fig. 3(a). For a fixed anode voltage, both anode and gate current increase with the increase in gate voltage, much more quickly from 70 V and saturates at about 90 V. On the other hand, the anode current increases but the gate current decreases with the increase in anode voltage. Defining the ratio of the anode current and the total current (gate current plus anode current) as the emission efficiency, the calculated emission efficiency at different gate voltages and anode voltages is shown in Fig. 3(b). The emission efficiency is higher at a higher anode voltage, and the highest efficiency for different anode voltages occurs at a gate voltage in the range 72-77 V. The highest efficiency is close to 100% and the corresponding anode current is 1.2 mA, which are obtained at an anode voltage of 1800 V and gate voltage of 77 V. From the curve, we could see that the highest anode current is 3.77 mA at the anode voltage of 1800 V and gate voltage of 100 V and no emission current saturation was observed. The anode current per pixel inferred is 37.7 μ A, which is close to that of Canon and Toshiba's surface-conduction electron-emitter display (SED) (40 μ A anode current). But

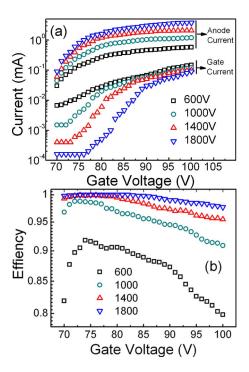


FIG. 3. (Color online) (a) The dependencies of the anode current and the gate current on gate voltage at different anode voltages. (b) Emission efficiency at different gate voltages and anode voltages.

our current density is larger than Canon and Toshiba's. The current density reported by Canon and Toshiba is about 3 mA/cm² at a very high gate field of 3666 V/ μ m, whereas a current density of 4.7 mA/cm² at a much lower gate field of 1 V/ μ m was obtained for our SCFE. It is worth mentioning that the current density is also significantly improved compared to the report of Li and co-workers 6-8,10 on SCFE using pure ZNTs (0.6 mA/cm²). Electron scattering is the main reason for SCFE, which can be explained as follows. Figure 4 is our electron trajectory simulation in a SCFE planar triode. The difference between the three pictures is the emitter material. In Fig. 4(a), there is no emitter between the cathode and the gate. As a result, when the gate voltage and the anode voltage are applied, only a little electron is emitted to the anode. When tetrapodlike ZnO is introduced as the emitter, a significant improvement of the electron sum is observed in Fig. 4(b). From the picture, we could see that the primary electrons emit from the cathode due to the strong

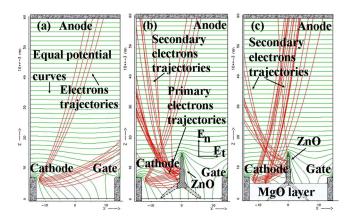


FIG. 4. (Color online) Simulations in the SCFE structure by different emitters. (a) None of the emitters. (b) Using a ZNT emitter. (c) Using a ZNT and a MgO nanoparticle composite emitter.

electric field between the cathode and the gate. When these primary electrons bombard on the ZNT, secondary electrons are generated. Because the tetrapodlike ZnO nanostructures have many protrusions, the longitude electric field (E_n) is formed around the ZNT, and part of the secondary electrons is collected by the anode. In order to increase the current efficiency, MgO is added into ZnO in Fig. 4(c). As randomicity of the orientation of electron scattering, part of the loss electrons from the ZnO could bombard on the MgO layer and excite other secondary electrons from MgO. These secondary electrons could emit to the anode again because a very high yield of secondary electron emission (SEE) could be obtained as we used a MgO nanopowder film with a lot of voids (Fig. 2). Such a porous MgO film benefits generation of strong localized electric field, which further enhances the SEE. 13,14 Meanwhile, adding MgO into ZnO makes the film more resistive, rendering a near zero gate current. It is worth mentioning that the emission can be turned off immediately when the applied gate voltage is removed, which is different from the cathode of Geis et al.^{4,5} This is because we have no electrodes under a glass substrate, i.e., no charge trapping in glass to maintain the voltage. In fact, the instant turn-off property is an advantage for the cathode being used in the FED panel with fast response time.

However, to realize an improvement of close to an order in current density (from 0.6 to 4.7 mA/cm²), there are other reasons that contribute to the enhancement of current density. One of the possible reasons may be triple junction, which plays a crucial role as the facilitator. In our configuration, a triple junction is formed between Ag, MgO/ZnO film, and vacuum, ¹⁵ making a high field enhancement. ^{16,17} However, an in-depth study will be discussed in our upcoming research.

Making use of this SCFE, a fully sealed 10×10 FED panel was fabricated. The test was carried out in a vacuum system with base pressure of 5×10^{-5} Pa. From both cathodes, addressing of individual pixel and display of characters and moving images were achieved. Figure 5 shows some Arabic numerals displayed on the panel. Each pixel could be accurately addressed and no interference is observed. The full emission image shown in Fig. 4 indicates that a good uniformity has been achieved.

In conclusion, we reported a surface emission cathode with high emission efficiency close to 100% and a large current about 3.7 mA with the gate voltage of 100 V. Low operation voltage was also obtained. 10×10 FED panel was fabricated to examine the practicability of the cathode. Testing experiment showed a uniform emission without crosstalk. This easy fabrication process provides a way to fabricate practical FED panel with high display quality.

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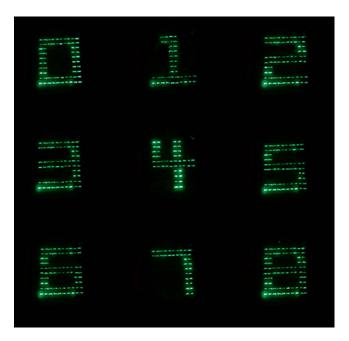


FIG. 5. (Color online) Arabic numerals displayed by the SCFE.

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